

	Application No.	Applicant(s)
Notice of Allowability		
	09/901,740 Examiner	ROGENMOSER ET AL. Art Unit
	Terry D. Cunningham	2816
The MAILING DATE of this communication appeal claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap, or other appropriate communication IGHTS. This application is subject to	plication. If not included not will be mailed in due course. THIS
1. X This communication is responsive to the amendment filed 28 March 2005.		
2. The allowed claim(s) is/are 26 and 27.		
3. 🔀 The drawings filed on <u>09 July 2001</u> are accepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority uner a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). 	e been received. e been received in Application No	
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give	itted. Note the attached EXAMINER es reason(s) why the oath or declara	'S AMENDMENT or NOTICE OF ation is deficient.
 CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers to Paper No./Mail Date including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the post of the p	son's Patent Drawing Review (PTO- s Amendment / Comment or in the C .84(c)) should be written on the drawing he header according to 37 CFR 1.121(sit of BIOLOGICAL MATERIAL r	Office action of ngs in the front (not the back) of (d). must be submitted. Note the
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. ☑ Interview Summary Paper No./Mail Da 08), 7. ☑ Examiner's Amendr	Patent Application (PTO-152) (PTO-413), te ment/Comment ent of Reasons for Allowance

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Continued Examination Under 37 CFR 1.114

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 28 March 2005 has been entered.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The following changes were made in the claims:

In claim 1, line 7, --directly-- was inserted following "voltage" and line 16, --directly-- was inserted following "voltage".

Authorization for this examiner's amendment was given in a telephone interview with William Kidd on May 4, 2005.

STATEMENT OF REASONS FOR ALLOWANCE

Examiner has considered Applicant's remarks for the outstanding rejection. It is clear that Applicant wishes to recite a direct connection between the "first default circuit" and "second default circuit", the "first node" and "second node" and the NAND gate. However, the amendment filed 28 March 2005 still allowed for an intervening element between the default circuits and the nodes. With the above amendment, it is now clearly recited that the default circuits directly apply the "default supply voltage" to the nodes and that the nodes are directly

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connected to NAND gate. Since the reference to Bechade includes inverters 24, it is now seen

that such no longer meets the claim language. Therefore, the claims are now in condition for

allowance.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Terry Cunningham whose telephone number is 571-272-1742.

The examiner can normally be reached on Monday-Thursday from 7:30 to 6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Timothy P. Callahan can be reached on 571-272-1740. The fax phone number for

the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TC

May 10, 2005

erry D. Cunningham

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Primary Examiner

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